

Details of Siemens EDA

Sr. No.	Product Description
SMDP HEP (Higher Education Programme) Bundle Consist of	
A	HEP Bundle for Analog and Digital Design Full Flow along with PCB System Design Solution
1.	IC Nanometer Design Bundle (Tanner S-Edit, L-Edit, T-Spice, Eldo, Questa ADMS, Nitro-SoC, Oasys-RTL, Calibre)
2.	Design Verification Test Bundle (Catapult Ultra, Vista, ReqTracer, Questa (including ModelSim), Oasys-RTL, Precision Synthesis, LeonardoSpectrum ASIC, Tessent Silicon Test, System Vision)
3.	PCB System Design and Analysis (PADS Professional with HyperLynx)
B	On Demand Training (Online Training) Bundle including Lab content and Material consist of all Mentor Technologies
1.	IC Logic Design - (HDL Designer, Req Tracer, Design Languages VHDL/Verilog/SV/UVM etc)
2.	IC Logic Verification (Questa and all verification tools - CDC/Fomal/Lint etc)
3.	Hardware Assisted Verification - Emulation - (Veloce)
4.	High Level Synthesis - (Catapult)
5.	IC Analog / Mixed Signal Verification - (Eldo/AFS/Questa ADMS etc)
6.	Physical Verification (Caliber)
7.	Design for Test (DFT - Tessent)
8.	Analog and Custom Layout Solution (Tanner)
9.	PCB solutions (PADS and Xpedition)
10.	Analysis Solution - SI/PI/Thermal - (Hyperlynx Solution)
11.	Valor NPI - DFM Solution
12.	IC Packaging - (Xpedition ICP)